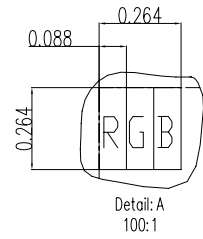
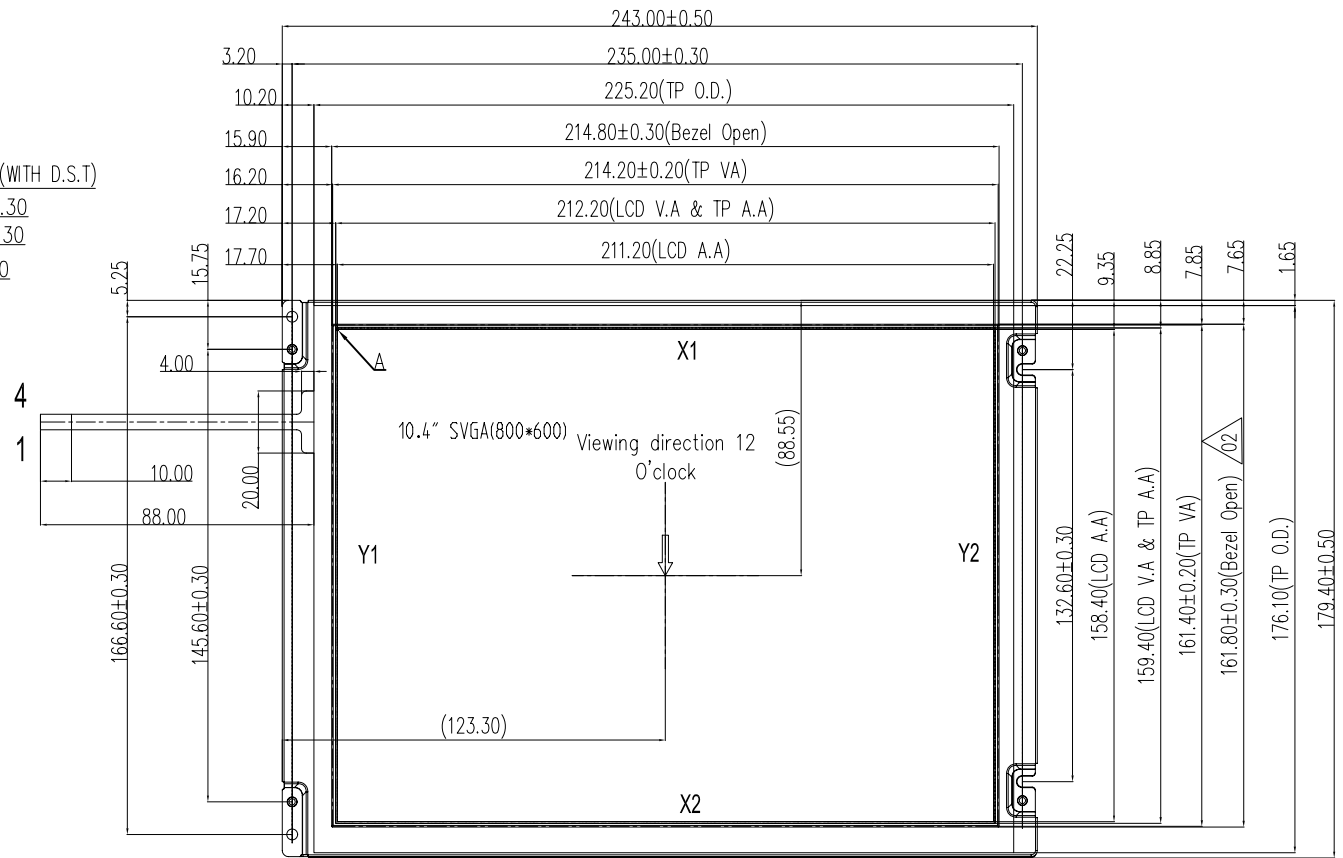
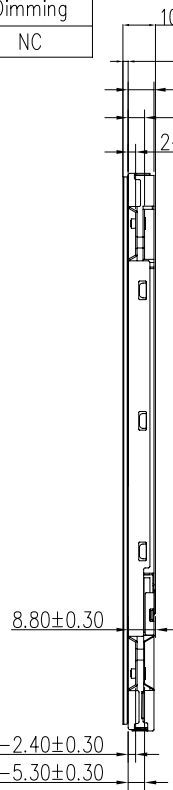




CN1		CN2	
Pin No.	Symbol	Pin No.	Symbol
1	VDD	1	VCC
2	VDD	2	GND
3	GND	3	EN
4	DPS	4	Dimming
5	RxIN0-	5	NC
6	RxIN0+		
7	GND		
8	RxIN1-		
9	RxIN1+		
10	GND		
11	RxIN2-		
12	RxIN2+		
13	GND		
14	RxCLKIN-		
15	RxCLKIN+		
16	GND		
17	RxIN3-		
18	RxIN3+		
19	AG Mode		
20	SEL68		

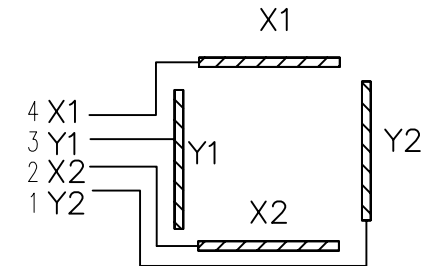
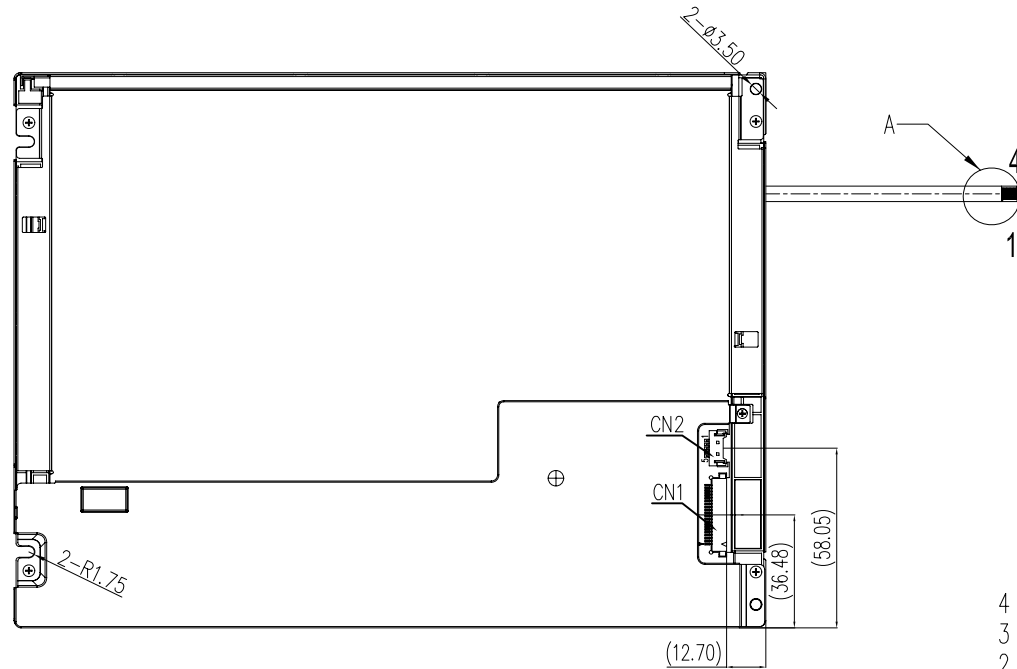
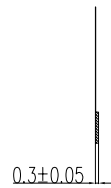
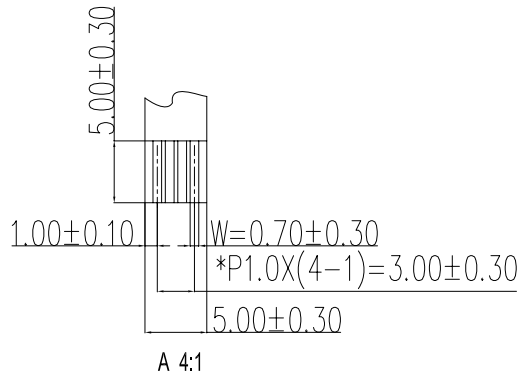
PIN	OUT
1	Y2
2	X2
3	Y1
4	X1



- NOTES:
1. Display Type: a-Si TFT;
 2. Backlight: 40-chips LED, 5 series, 8 Parallel;
 3. Gray Scale Inversion: 6 o'clock;
 4. Driver IC: Gate IC NT39211, Source IC NT39411;
 5. Matched connector: CN1 DF19G-20S-1C(or compatible); CN2 H208K-P05N-02B(or compatible);
 6. General Tolerance: ±0.3;
 7. Requirements on environment protection: RoHS compliant;
 8. Recommended Case Open Area should be less than Module V.A.

MULTI-INNO TECHNOLOGY CO.,LTD.			
DRAWN BY:	CAROL 2014.10.29	SCALE : 1/1	MODULE P/N:
CHECKED BY:		UNIT: mm	MI1040GT-10
APPROVED BY:			DESCRIPTION:
VERSION NO:	02		TFT MODULE

VER.	REVISED DESCRIPTION	DATE
02	Update dimension	2014.10.29
01	First issue	2012.12.21



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DRAWN BY:	CAROL 2014.10.29	SCALE : 1/1	MODULE P/N:
CHECKED BY:		UNIT: mm	MI1040GT-10
APPROVED BY:			DESCRIPTION:
VERSION NO:	02		TFT MODULE

02	Update dimension	2014.10.29
01	First issue	2012.12.21
VER.	REVISED DESCRIPTION	DATE